

Notice of References Cited

Application/Control No.

10/004,961

Applicant(s)/Patent Under
Reexamination
HOLDER ET AL.

Examiner

Matthew A. Anderson

Art Unit

1765

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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
	D	US-			
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf et al, Silicon Processing for the VLSI Era, Volume 1 Processing Control, Lattice Press, Sunset Beach , CA, USA, pp 36-72, 1986.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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